

### FEATURES

- High performance, dual axis accelerometer on a single IC chip
- 5 mm  $\times$  5 mm  $\times$  2 mm LCC package
- Better than 2 mg resolution at 60 Hz
- Low power: 500  $\mu$ A at  $V_s = 3.3$  V (typical)
- High zero  $g$  bias stability
- High sensitivity accuracy
- $-40^\circ\text{C}$  to  $+125^\circ\text{C}$  temperature range
- X- and Y- axes aligned to within  $0.1^\circ$  (typical)
- BW adjustment with a single capacitor
- Single-supply operation
- 3,500  $g$  shock survival

### APPLICATIONS

- Vehicle dynamic control (VDC)/  
electronic stability program  
(ESP) systems
- Electronic chassis control
- Electronic braking
- Platform stabilization/leveling
- Navigation
- Alarms and motion detectors
- High accuracy, 2-axis tilt sensing

### GENERAL DESCRIPTION

The ADXL204 is a high precision, low power, complete dual-axis accelerometer with signal-conditioned voltage outputs, all on a single monolithic IC. The ADXL204 measures acceleration with a full-scale range of  $\pm 1.7 g$ . The ADXL204 can measure both dynamic acceleration (for example, vibration) and static acceleration (for example, gravity).

The typical noise floor is  $170 \mu\text{g}/\sqrt{\text{Hz}}$ , allowing signals below 2 mg ( $0.1^\circ$  of inclination) to be resolved in tilt sensing applications using narrow bandwidths ( $< 60$  Hz).

The user selects the bandwidth of the accelerometer using capacitors  $C_X$  and  $C_Y$  at the  $X_{\text{OUT}}$  and  $Y_{\text{OUT}}$  pins. Bandwidths of 0.5 Hz to 2.5 kHz can be selected to suit the application.

The ADXL204 is available in a 5 mm  $\times$  5 mm  $\times$  2 mm, 8-pad hermetic LCC package.

### FUNCTIONAL BLOCK DIAGRAM

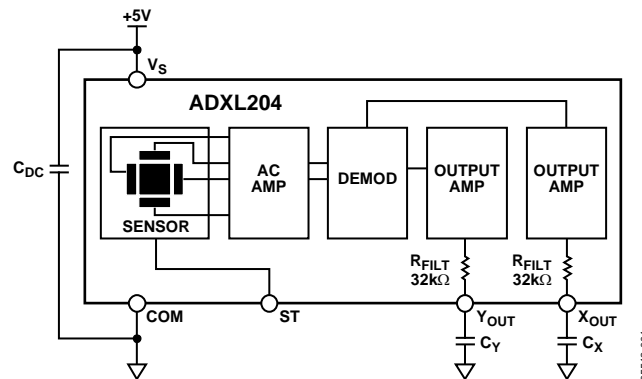


Figure 1. ADXL204 Functional Block Diagram

### Rev. 0

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## REVISION HISTORY

4/05—Revision 0: Initial Version

# SPECIFICATIONS

All minimum and maximum specifications are guaranteed. Typical specifications are not guaranteed.

$T_A = -40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ ;  $V_S = 3.3\text{ V}$ ;  $C_X = C_Y = 0.1\ \mu\text{F}$ ; Acceleration = 0 g, unless otherwise noted.

**Table 1.**

Parameter	Conditions	Min	Typ	Max	Unit
SENSOR INPUT	Each Axis				
Measurement Range <sup>1</sup>		$\pm 1.7$			g
Nonlinearity	% of Full Scale		$\pm 0.5$	$\pm 2.5$	%
Package Alignment Error			$\pm 1$		Degrees
Alignment Error	X Sensor to Y Sensor		$\pm 0.1$		Degrees
Cross Axis Sensitivity			$\pm 2$	$\pm 5$	%
SENSITIVITY (Ratiometric) <sup>2</sup>	Each Axis				
Sensitivity at $X_{OUT}, Y_{OUT}$	$V_S = 3.3\text{ V}$	589	620	651	mV/g
Sensitivity Change Due to Temperature <sup>3</sup>	$V_S = 3.3\text{ V}$		$\pm 0.3$		%
ZERO g BIAS LEVEL (Ratiometric)	Each Axis				
0 g Voltage at $X_{OUT}, Y_{OUT}$	$V_S = 3.3\text{ V}$	1.55	1.65	1.75	V
Initial 0 g Output Deviation from Ideal	$V_S = 3.3\text{ V}, 25^{\circ}\text{C}$		$\pm 50$		mg
0 g Offset vs. Temperature			$\pm 0.2$		mg/ $^{\circ}\text{C}$
NOISE PERFORMANCE					
Output Noise	$< 4\text{ kHz}, V_S = 3.3\text{ V}, 25^{\circ}\text{C}$		1	6	mV rms
Noise Density	@ $25^{\circ}\text{C}$		170		$\mu\text{g}/\sqrt{\text{Hz}}$ rms
FREQUENCY RESPONSE <sup>4</sup>					
$C_X, C_Y$ Range <sup>5</sup>		0.002		10	$\mu\text{F}$
$R_{\text{FILT}}$ Tolerance		24	32	40	k $\Omega$
Sensor Resonant Frequency			5.5		kHz
SELF TEST <sup>6</sup>					
Logic Input Low				0.66	V
Logic Input High		2.64			V
ST Input Resistance to Ground		30	50		k $\Omega$
Output Change at $X_{OUT}, Y_{OUT}$	Self Test 0 to 1	100	200	300	mV
OUTPUT AMPLIFIER					
Output Swing Low	No Load		0.2		V
Output Swing High	No Load		2.9		V
POWER SUPPLY					
Operating Voltage Range		3		6	V
Quiescent Supply Current			0.5	0.9	mA
Turn-On Time <sup>7</sup>			20		ms

<sup>1</sup> Guaranteed by measurement of initial offset and sensitivity.

<sup>2</sup> Sensitivity is essentially ratiometric to  $V_S$ . For  $V_S = 3.0\text{ V}$  to  $3.6\text{ V}$ , sensitivity is typically 185 mV/V/g to 190 mV/V/g.

<sup>3</sup> Defined as the change from ambient-to-maximum temperature or ambient-to-minimum temperature.

<sup>4</sup> Actual frequency response controlled by user-supplied external capacitor ( $C_X, C_Y$ ).

<sup>5</sup> Bandwidth =  $1/(2 \times \pi \times 32\text{ k}\Omega \times C)$ . For  $C_X, C_Y = 0.002\ \mu\text{F}$ , Bandwidth = 2500 Hz. For  $C_X, C_Y = 10\ \mu\text{F}$ , Bandwidth = 0.5 Hz. Minimum/maximum values are not tested.

<sup>6</sup> Self-test response changes cubically with  $V_S$ .

<sup>7</sup> Larger values of  $C_X, C_Y$  will increase turn-on time. Turn-on time is approximately  $160 \times C_X$  or  $C_Y + 4\text{ ms}$ , where  $C_X, C_Y$  are in  $\mu\text{F}$ .

# ADXL204

## ABSOLUTE MAXIMUM RATINGS

Table 2.

Parameter	Rating
Acceleration (Any Axis, Unpowered)	3,500 g
Acceleration (Any Axis, Powered)	3,500 g
Drop Test (Concrete Surface)	1.2 m
V <sub>s</sub> All Other Pins	-0.3 V to +7.0 V (COM - 0.3 V) to (V <sub>s</sub> + 0.3 V)
Output Short-Circuit Duration (Any Pin to Common)	Indefinite
Operating Temperature Range	-55°C to +125°C
Storage Temperature	-65°C to +150°C

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Table 3. Package Characteristics

Package Type	$\theta_{JA}$	$\theta_{JC}$	Device Weight
8-Lead CLCC	120°C/W	20°C/W	<1.0 gram

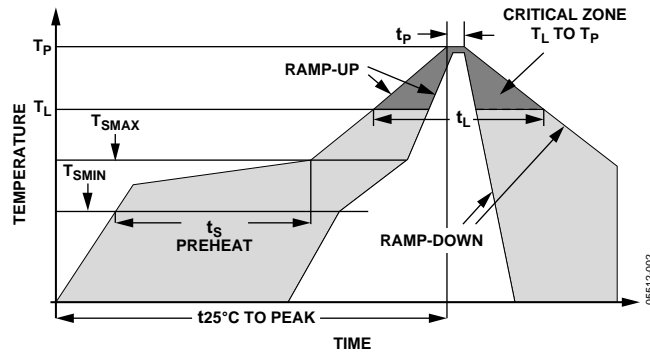


Figure 2. Recommended Soldering Profile

Table 4.

Profile Feature	Condition	
	Sn63/Pb37	Pb-Free
Average Ramp Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/sec Max	
Preheat		
Minimum Temperature (T <sub>S MIN</sub> )	100°C	150°C
Minimum Temperature (T <sub>S MAX</sub> )	150°C	200°C
Time (T <sub>S MIN</sub> to T <sub>S MAX</sub> ) (t <sub>s</sub> )	60–120 sec	60–150 sec
T <sub>S MAX</sub> to T <sub>L</sub>		
Ramp-Up Rate	3°C/sec	
Time Maintained Above Liquidous (T <sub>L</sub> )		
Liquidous Temperature (T <sub>L</sub> )	183°C	217°C
Time (t <sub>L</sub> )	60–150 sec	60–150 sec
Peak Temperature (T <sub>P</sub> )	240°C +0°C/-5°C	260°C +0°C/-5°C
Time Within 5°C of Actual Peak Temperature (t <sub>p</sub> )	10–30 sec	20–40 sec
Ramp-Down Rate	6°C/sec Max	
Time 25°C to Peak Temperature	6 minutes max	8 minutes max

## ESD CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



## PIN CONFIGURATION AND FUNCTIONAL DESCRIPTIONS

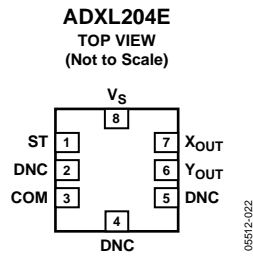


Figure 3. ADXL204 8-Lead CLCC

Table 5. Pin Function Descriptions

Pin No.	Mnemonic	Description
1	ST	Self-Test
2	DNC	Do Not Connect
3	COM	Common
4	DNC	Do Not Connect
5	DNC	Do Not Connect
6	Y <sub>OUT</sub>	Y Channel Output
7	X <sub>OUT</sub>	X Channel Output
8	V <sub>S</sub>	3 V to 6 V

## TYPICAL PERFORMANCE CHARACTERISTICS

VS = 3.3 V for all graphs, unless otherwise noted.

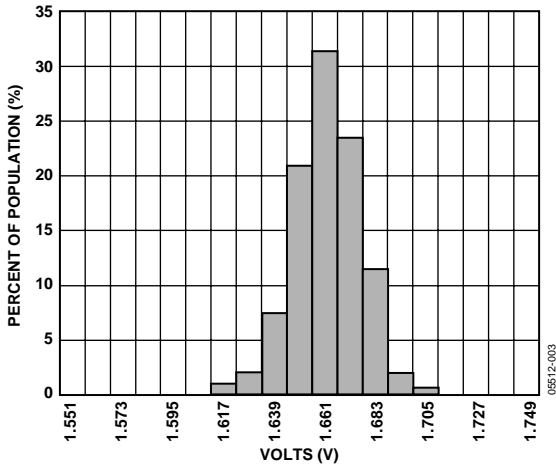


Figure 4. X-Axis Zero g Bias Output at 25°C

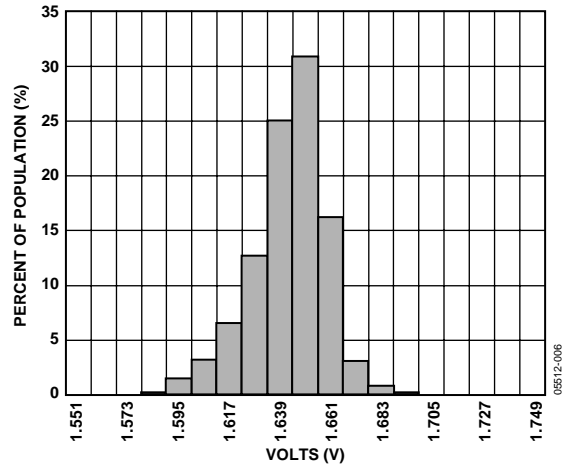


Figure 7. Y-Axis Zero g Bias Output at 25°C

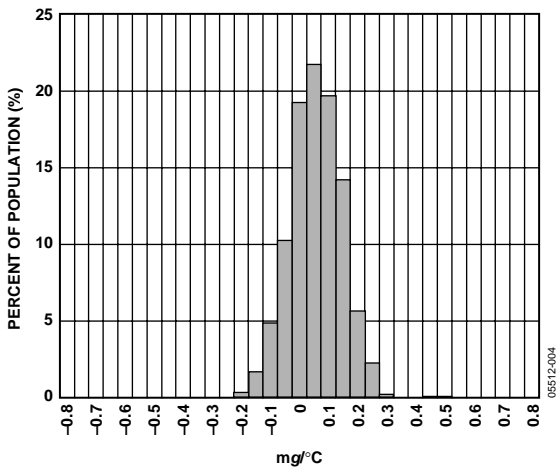


Figure 5. X-Axis Zero g Bias Tempco

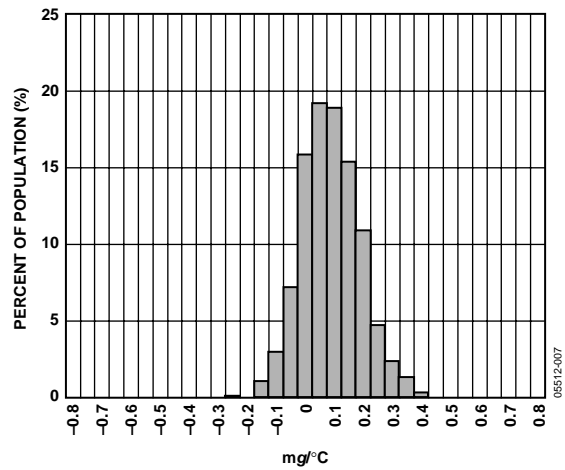


Figure 8. Y-Axis Zero g Bias Tempco

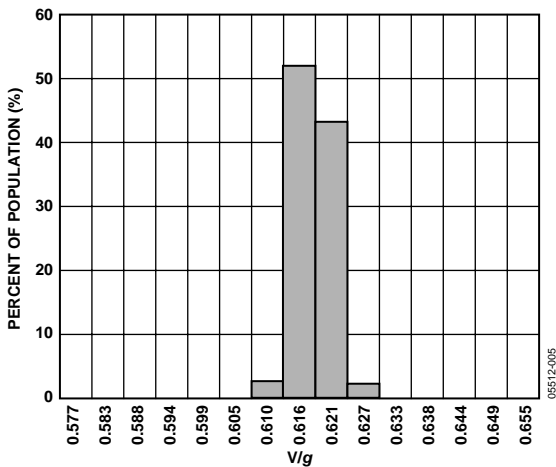


Figure 6. X-Axis Sensitivity at 25°C

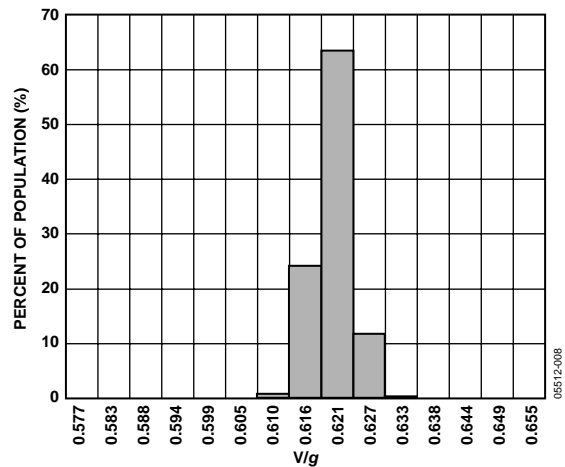


Figure 9. Y-Axis Sensitivity at 25°C

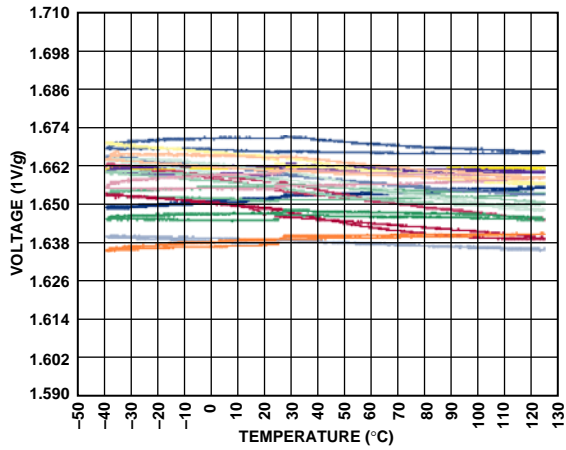


Figure 10. Zero g Bias vs. Temperature – Parts Soldered to PCB

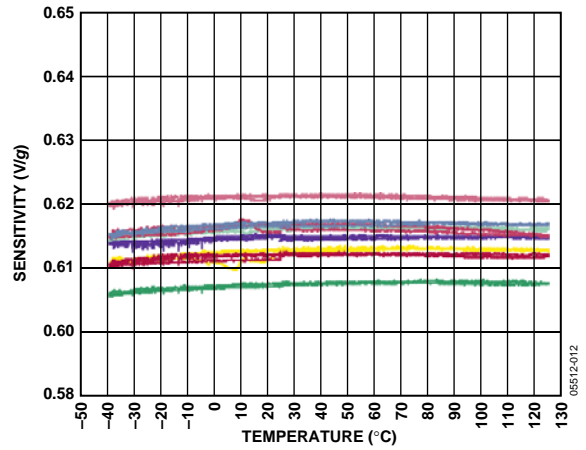


Figure 13. Sensitivity vs. Temperature – Parts Soldered to PCB

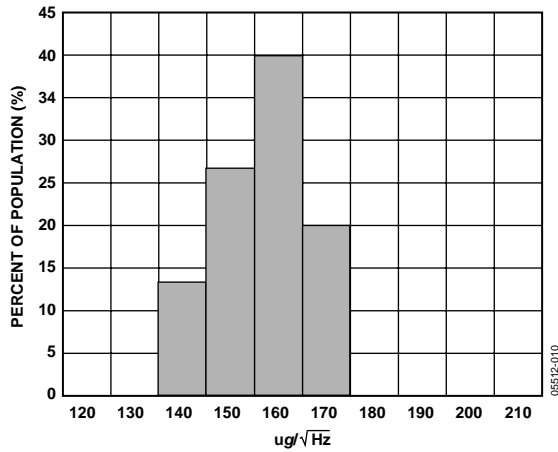


Figure 11. X-Axis Noise Density at 25°C

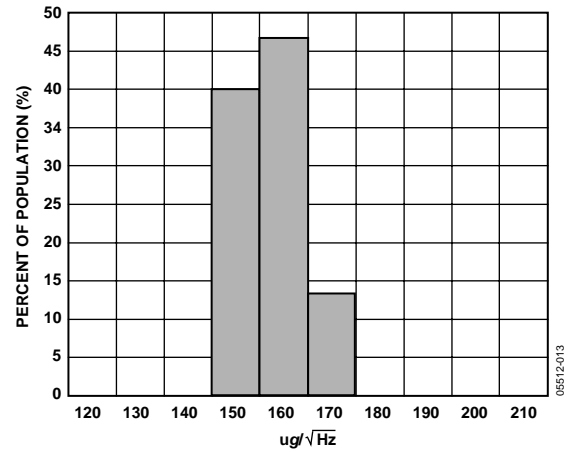


Figure 14. Y-Axis Noise Density at 25°C

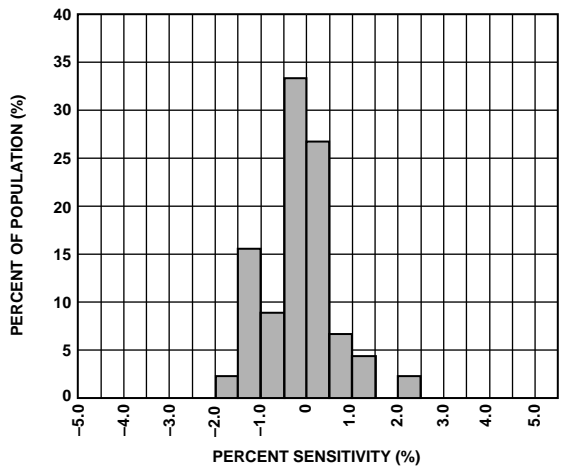


Figure 12. Z vs. X Cross-Axis Sensitivity

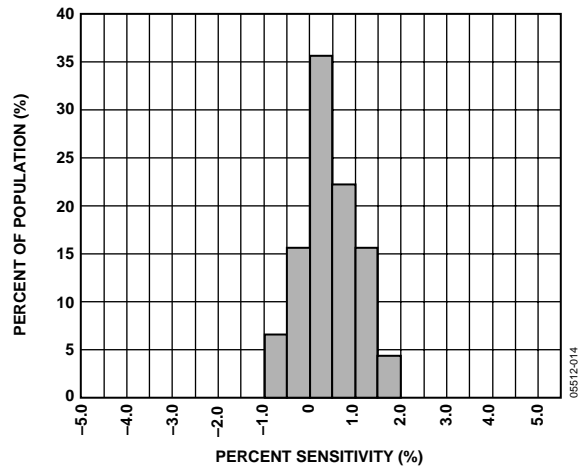


Figure 15. Z vs. Y Cross-Axis Sensitivity

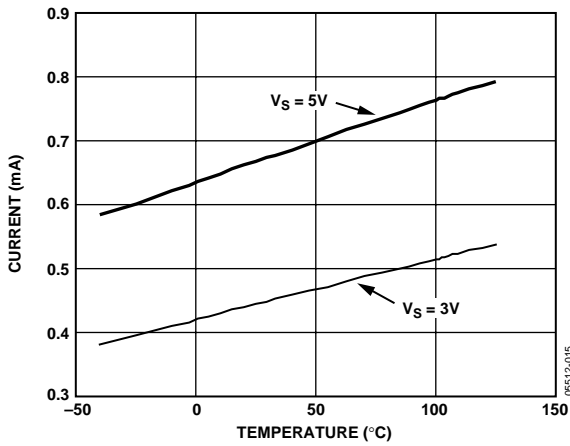


Figure 16. Supply Current vs. Temperature

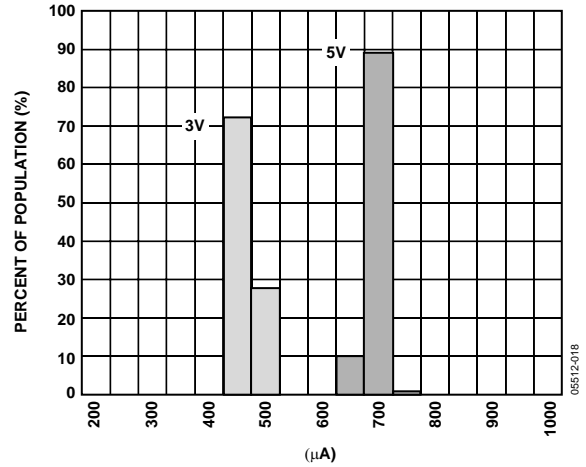


Figure 19. Supply Current at 25°C

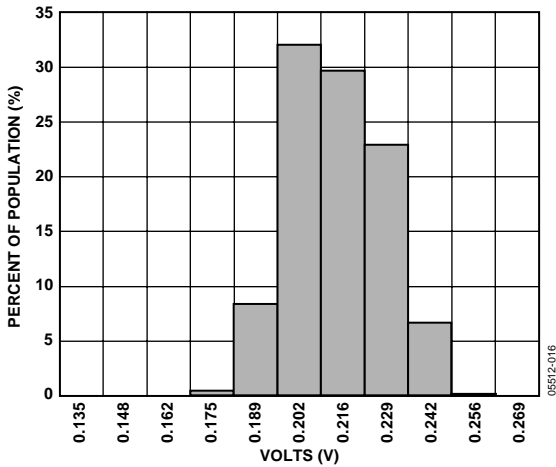


Figure 17. X-Axis Self-Test Response at 25°C

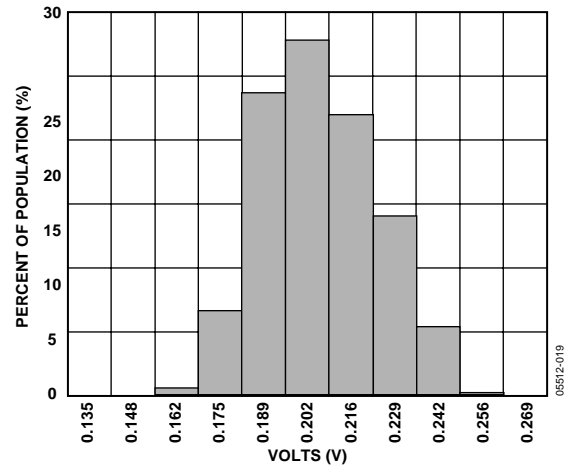


Figure 20. Y-Axis Self-Test Response at 25°C

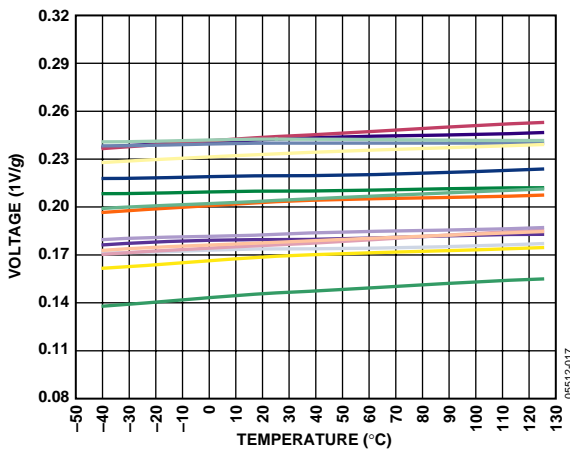


Figure 18. Self-Test Response vs. Temperature

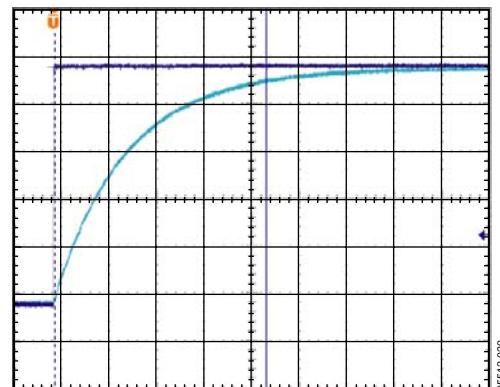


Figure 21. Turn-On Time – C<sub>x</sub>, C<sub>y</sub> = 0.1 μF, Time Scale = 2 ms/div

## THEORY OF OPERATION

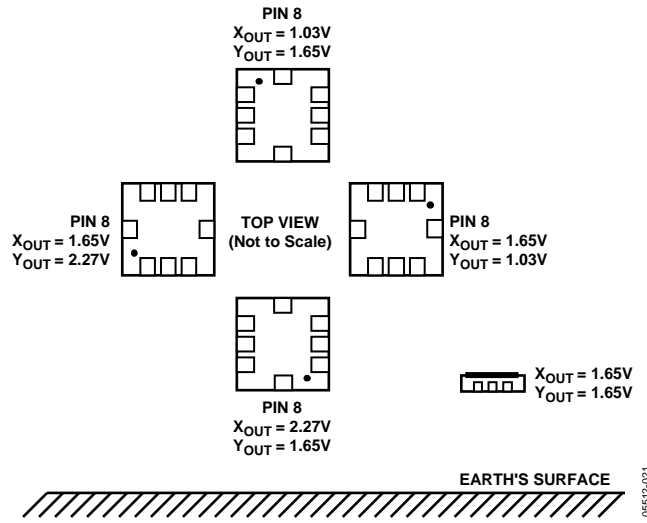


Figure 22. Output Response vs. Orientation

The ADXL204 is a complete acceleration measurement system on a single monolithic IC. The ADXL204 is a dual axis accelerometer. It contains a polysilicon surface-micromachined sensor and signal conditioning circuitry to implement an open-loop acceleration measurement architecture. The output signals are analog voltages proportional to acceleration. The ADXL204 is capable of measuring both positive and negative accelerations to at least  $\pm 1.7 g$ . The accelerometer can measure static acceleration forces such as gravity, allowing it to be used as a tilt sensor.

The sensor is a surface-micromachined polysilicon structure built on top of the silicon wafer. Polysilicon springs suspend the structure over the surface of the wafer and provide a resistance against acceleration forces. Deflection of the structure is measured using a differential capacitor that consists of independent fixed plates and plates attached to the moving mass. The fixed plates are driven by  $180^\circ$  out-of-phase square waves. Acceleration will deflect the beam and unbalance the differential capacitor, resulting in an output square wave whose amplitude is proportional to acceleration. Phase-sensitive demodulation techniques are then used to rectify the signal and determine the direction of the acceleration.

The output of the demodulator is amplified and brought off-chip through a  $32 k\Omega$  resistor. At this point, the user can set the signal bandwidth of the device by adding a capacitor. This filtering improves measurement resolution and helps prevent aliasing.

## PERFORMANCE

Rather than using additional temperature compensation circuitry, innovative design techniques have been used to ensure high performance is built in. As a result, there is essentially no quantization error or nonmonotonic behavior, and temperature hysteresis is very low, typically less than  $10 mg$  over the  $-40^\circ C$  to  $+125^\circ C$  temperature range.

Figure 10 shows the zero  $g$  output performance of eight parts (X and Y-axis) over a  $-40^\circ C$  to  $+125^\circ C$  temperature range.

Figure 13 demonstrates the typical sensitivity shift over temperature for  $V_s = 3.3 V$ . Sensitivity stability is typically better than  $\pm 1\%$  over temperature.

## APPLICATIONS

### POWER SUPPLY DECOUPLING

For most applications, a single 0.1  $\mu\text{F}$  capacitor,  $C_{\text{DC}}$ , adequately decouples the accelerometer from noise on the power supply. However in some cases, particularly where noise is present at the 140 kHz internal clock frequency (or any harmonic thereof), noise on the supply may cause interference on the ADXL204 output. If additional decoupling is needed, a 100  $\Omega$ , or smaller, resistor or ferrite bead may be inserted in the supply line of the ADXL204. Additionally, a larger bulk bypass capacitor, in the 1  $\mu\text{F}$  to 22  $\mu\text{F}$  range, can be added in parallel to  $C_{\text{DC}}$ .

### SETTING THE BANDWIDTH USING $C_X$ AND $C_Y$

The ADXL204 has provisions for bandlimiting the  $X_{\text{OUT}}$  and  $Y_{\text{OUT}}$  pins. Capacitors must be added at these pins to implement low-pass filtering for antialiasing and noise reduction. The equation for the 3 dB bandwidth is

$$F_{-3\text{dB}} = 1/(2\pi(32\text{ k}\Omega) \times C_{(X, Y)})$$

or more simply,

$$F_{-3\text{dB}} = 5\ \mu\text{F}/C_{(X, Y)}$$

The tolerance of the internal resistor ( $R_{\text{FILT}}$ ) can vary typically as much as  $\pm 25\%$  of its nominal value (32 k $\Omega$ ); thus, the bandwidth will vary accordingly. A minimum capacitance of 2000 pF for  $C_X$  and  $C_Y$  is required in all cases.

**Table 6. Filter Capacitor Selection,  $C_X$  and  $C_Y$**

Bandwidth (Hz)	Capacitor ( $\mu\text{F}$ )
1	4.7
10	0.47
50	0.10
100	0.05
200	0.027
500	0.01

### SELF-TEST

The ST pin controls the self-test feature. When this pin is set to  $V_S$ , an electrostatic force is exerted on the beam of the accelerometer. The resulting movement of the beam allows the user to test if the accelerometer is functional. The typical change in output will be 325 mg (corresponding to 200 mV). This pin may be left open-circuit or connected to common in normal use.

The ST pin should never be exposed to voltage greater than  $V_S + 0.3\text{ V}$ . If the system design is such that this condition cannot be guaranteed (that is, multiple supply voltages present), a low  $V_F$  clamping diode between ST and  $V_S$  is recommended.

### DESIGN TRADE-OFFS FOR SELECTING FILTER CHARACTERISTICS: THE NOISE/BW TRADE-OFF

The accelerometer bandwidth selected will ultimately determine the measurement resolution (smallest detectable acceleration). Filtering can be used to lower the noise floor, which improves the resolution of the accelerometer. Resolution is dependent on the analog filter bandwidth at  $X_{\text{OUT}}$  and  $Y_{\text{OUT}}$ .

The output of the ADXL204 has a typical bandwidth of 2.5 kHz. The user must filter the signal at this point to limit aliasing errors. The analog bandwidth must be no more than half the A/D sampling frequency to minimize aliasing. The analog bandwidth may be further decreased to reduce noise and improve resolution.

The ADXL204 noise has the characteristics of white Gaussian noise, which contributes equally at all frequencies and is described in terms of  $\mu\text{g}/\sqrt{\text{Hz}}$  (that is, the noise is proportional to the square root of the accelerometer's bandwidth). The user should limit bandwidth to the lowest frequency needed by the application in order to maximize the resolution and dynamic range of the accelerometer.

With the single pole roll-off characteristic, the typical noise of the ADXL204 is determined by

$$\text{rmsNoise} = (170\ \mu\text{g}/\sqrt{\text{Hz}}) \times (\sqrt{\text{BW}} \times 1.6)$$

At 100 Hz the noise will be

$$\text{rmsNoise} = (170\ \mu\text{g}/\sqrt{\text{Hz}}) \times (\sqrt{\text{BW}} \times 1.6) = 2.15\ \text{mg}$$

Often, the peak value of the noise is desired. Peak-to-peak noise can only be estimated by statistical methods. Table 7 is useful for estimating the probabilities of exceeding various peak values, given the rms value.

**Table 7. Estimation of Peak-to-Peak Noise**

Peak-to-Peak Value	% of Time Noise Exceeds Nominal Peak-to-Peak Value
$2 \times \text{rms}$	32
$4 \times \text{rms}$	4.6
$6 \times \text{rms}$	0.27
$8 \times \text{rms}$	0.006

Peak-to-peak noise values give the best estimate of the uncertainty in a single measurement. Table 8 gives the typical noise output of the ADXL204 for various  $C_X$  and  $C_Y$  values.

**Table 8. Filter Capacitor Selection ( $C_X$ ,  $C_Y$ )**

Bandwidth(Hz)	$C_X$ , $C_Y$ ( $\mu$ F)	RMS Noise (mg)	Peak-to-Peak Noise Estimate (mg)
10	0.47	0.7	4.1
50	0.1	1.5	9.1
100	0.047	2.2	12.9
500	0.01	4.8	28.8

### USING THE ADXL204 WITH OPERATING VOLTAGES OTHER THAN 3.3 V

The ADXL204 is tested and specified at  $V_S = 3.3$  V; however, it can be powered with  $V_S$  as low as 3 V or as high as 6 V. Some performance parameters will change as the supply voltage is varied.

The ADXL204 output is ratiometric, so the output sensitivity, or scale factor, will vary proportionally to supply voltage. At  $V_S = 3$  V the output sensitivity is typically 560 mV/g. At  $V_S = 5$  V the output sensitivity is typically 1000 mV/g.

The zero g bias output is also ratiometric, so the zero g output is nominally equal to  $V_S/2$  at all supply voltages.

The output noise is not ratiometric but is absolute in volts; therefore, the noise density decreases as the supply voltage increases. This is because the scale factor (mV/g) increases while the noise voltage remains constant. At  $V_S = 3$  V, the noise density is typically 190  $\mu$ g/ $\sqrt{\text{Hz}}$ . At  $V_S = 5$  V, the noise density is typically 110  $\mu$ g/ $\sqrt{\text{Hz}}$ .

Self-test response in g is roughly proportional to the square of the supply voltage. However, when ratiometricity of sensitivity is factored in with supply voltage, self-test response in volts is roughly proportional to the cube of the supply voltage. This means at  $V_S = 3$  V, the self-test response will be approximately equivalent to 150 mV, or equivalent to 270 mg (typical). At  $V_S = 5$  V, the self-test response will be approximately equivalent to 750 mV, or equivalent to 750 mg (typical).

The supply current decreases as the supply voltage decreases. Typical current consumption at  $V_{DD} = 5$  V is 750  $\mu$ A.

### USING THE ADXL204 AS A DUAL-AXIS TILT SENSOR

One of the most popular applications of the ADXL204 is tilt measurement. An accelerometer uses the force of gravity as an input vector to determine the orientation of an object in space.

An accelerometer is most sensitive to tilt when its sensitive axis is perpendicular to the force of gravity, that is, parallel to the earth's surface. At this orientation, its sensitivity to changes in tilt is highest. When the accelerometer is oriented on axis to gravity, i.e., near its +1 g or -1 g reading, the change in output acceleration per degree of tilt is negligible. When the accelerometer is perpendicular to gravity, its output will change nearly 17.5 mg per degree of tilt. At 45°, its output changes at only 12.2 mg per degree and resolution declines.

#### **Dual-Axis Tilt Sensor: Converting Acceleration to Tilt**

When the accelerometer is oriented, so both its X-axis and Y-axis are parallel to the earth's surface, it can be used as a 2-axis tilt sensor with a roll axis and a pitch axis. Once the output signal from the accelerometer has been converted to an acceleration that varies between -1 g and +1 g, the output tilt in degrees is calculated as follows:

$$PITCH = ASIN(A_X/1 g)$$

$$ROLL = ASIN(A_Y/1 g)$$

Be sure to account for overranges. It is possible for the accelerometers to output a signal greater than  $\pm 1$  g due to vibration, shock, or other accelerations.

# ADXL204

## OUTLINE DIMENSIONS

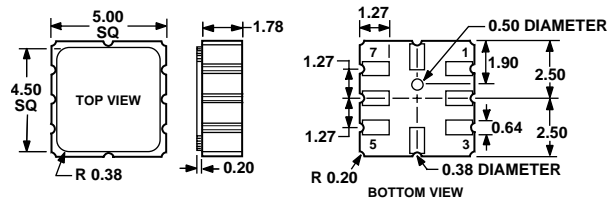


Figure 23. 8-Terminal Ceramic Leadless Chip Carrier [LCC]  
(E-8)

Dimensions shown in millimeters

## ORDERING GUIDE

Model	Number of Axes	Specified Voltage (V)	Temperature Range	Package Description	Package Option
ADXL204CE	2	3.3	-40°C to +125°C	8-Lead Ceramic Leadless Chip Carrier	E-8
ADXL204CE-REEL	2	3.3	-40°C to +125°C	8-Lead Ceramic Leadless Chip Carrier	E-8
ADXL204EB				Evaluation Board	